

1018 10/026241 12/21/01

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10026241	FILING DATE 12/21/2001	CLASS 327	SUBCLASS 478	GAU 2826	EXAMINER Zweizig DEPT. ADMIN. R.
**APPLICANTS: Theobald Thomas; Leipold Ludwig;					
<h1>BEST AVAILABLE COPY</h1>					
**CONTINUING DATA VERIFIED: none JZ					
** FOREIGN APPLICATIONS VERIFIED: GERMANY 100 64 123.7 12/21/2000 JZ					
PG-PUB <input type="checkbox"/>		DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>	
Foreign priority claimed 35 USC 119 conditions met		<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO WMP-IFT-007	
Verified and Acknowledged Examiners's initials					
TITLE : Circuit configuration for driving a semiconductor switching element and method for same <small>U.S. DEPT. OF COMM. / PAT. & TM-PTO-438L (Rev. 12-94)</small>					

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg. 3
<input type="checkbox"/> TERMINAL DISCLAIMER		Application Examiner	
		PREPARED FOR ISSUE	
WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.			

FILED WITH

☐ DISK (CRF)

☐ CD-ROM
(Attached in pocket on right inside flap)